



FOR IMMEDIATE RELEASE

MediaTek Deploys EdXact's Jivaro Technology for Analog Circuits

Hsin-Chu, Taiwan and Grenoble, France – January 25, 2010. Backend verification specialist [EdXact SA](#) and [MediaTek Inc.](#), a leading fabless semiconductor company for wireless communications and digital multimedia solutions today announced that they have entered a strategic relationship to deploy EdXact's netlist reduction platform for analog circuits.

"At MediaTek we will integrate [Jivaro](#) into our current design flows for our analog circuits in deep sub-micron processes. After verifying the tool on different designs, we were impressed by the reduction capabilities achieved while maintaining accuracy", said a representative of MediaTek's Design team. "With Jivaro we can carry out more simulations faster, which in return assures a high level of quality for our products."

"We are delighted to be chosen by MediaTek", said Mathias Silvant, president and CEO of EdXact. "In a very short time, MediaTek has become one of the most important developers of integrated circuits for mobile applications, mainly for the Chinese market. MediaTek's choice demonstrates the economical value of our technology for the development of low-cost, but high-quality devices."

The Jivaro model order based technology is the only independent industrial netlist reduction technology available, which applies on post-layout extracted netlists. Jivaro remodels and reduces the parasitic overhead in a way that the simulation tool in place is able to simulate larger netlists, or reduce the memory footprint and the execution time.

About MediaTek

MediaTek Inc. is a leading fabless semiconductor company for wireless communications and digital multimedia solutions. The company is a market leader and pioneer in cutting-edge SOC system solutions for wireless communications, high-definition TV, optical storage, and DVD and Blu-ray products. Founded in 1997 and listed on Taiwan Stock Exchange under the code "2454", MediaTek is headquartered in Taiwan and has sales and research subsidiaries in Mainland China, Singapore, India, U.S., Japan, Korea, Denmark and England. For more information, please visit MediaTek's website at www.mediatek.com.

About EdXact

Founded in 2004, EdXact SA focuses on electronic design tools aimed at physical verification tasks. EdXact's innovative model order reduction technology helps accelerate extensive backend verifications in complex IC design cycles. EdXact is headquartered in Grenoble area, France with sales offices in Japan, Korea, Israel, Taiwan and India. For additional information please visit EdXact's website at <http://www.edxact.com>