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January 26, 2010

MediaTek Inks Deal with EdXact to Deploy Jivaro Technology



By Jai C.S.

[TMCnet Contributor](#)

MediaTek Inc., a fabless semiconductor company for wireless communications and digital multimedia solutions, has [reportedly signed](#) a strategic alliance with EdXact, a company specializing in software editor for advanced tools in Electronic Design Automation, or "EDA," to deploy EdXact's Jivaro Technology for analog circuits.

Story continues below ↓

"At MediaTek we will integrate Jivaro into our current design flows for our analog circuits in deep sub-micron processes," said an official with MediaTek's Design team. "After verifying the tool on different designs, we were impressed by the reduction capabilities achieved while maintaining accuracy."

"With Jivaro we can carry out more simulations faster, which in return assures a high level of quality for our products," the official added.

EdXact's Jivaro technology and its related tools are recognized as the booster of all existing simulation and analysis tools in advanced nanometer design flows. It is a tool for the IC nanometer designs dedicated to the reduction of parasitic networks.

In addition, EdXact believes that its Jivaro model order based technology is the only independent industrial netlist reduction technology available, which applies on post-layout extracted netlists. Jivaro remodels and reduces the parasitic overhead in a way that the simulation tool in place is able to simulate larger netlists, or reduce the memory footprint and the execution time.

"In a very short time, MediaTek has become one of the most important developers of integrated circuits for mobile applications, mainly for the Chinese market," said Mathias Silvant, president and CEO at EdXact. "MediaTek's choice demonstrates the economical value of our technology for the development of low-cost, but high-quality devices."

Jai C.S. is a contributing editor for TMCnet. To read more of Jai's articles, please visit his [columnist page](#).

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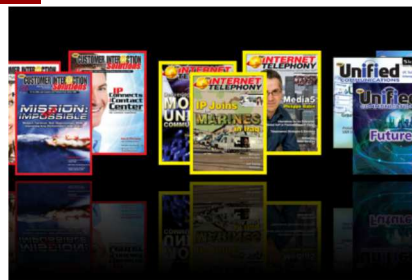
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