



PRESS RELEASE

## EdXact Selects ICON Design Automation as Sales Partner in India

June 2, 2009

**Grenoble, France** – Backend verification specialist EDXACT SA today announced that it has signed an exclusive multi-year distribution agreement with ICON Design Automation in India.

Under this agreement, ICON will distribute all of EDXACT's tools, including the netlist reduction tools Jivaro A (for analog and RF designs) and Jivaro D (for digital and mixed-signal circuits), and Comanche its parasitics rule checking analyzer.

ICON Design Automation Pvt Ltd ([www.icon-dapl.com](http://www.icon-dapl.com)) over the last decade has established itself as a Premier EDA Solutions provider to multinational and Indian design companies through its association with leading-edge EDA tool suppliers in the domains of:VLSI, Embedded, and RF/Microwave. ICON, headquartered in Bangalore, has offices in Hyderabad and Delhi.

« EDXACT's Jivaro™ technology has gained reputation to dramatically boost performances of current simulation and analysis tools in advanced nanometer design flows and we are delighted to be associated as their Indian Partner.» said R. Varalakshmi, CEO of ICON.

“Our Asian customer base continues to grow and we won't miss the promising opportunities of India” said Mathias Silvant, president and CEO of EDXACT. “India is a large country with several centers of gravity with respect to the semiconductor industry. ICON is active in all these places with both sales channel and strong team of field application engineers. By partnering with ICON we provide real-time support to our Indian semiconductors customers.

.The Jivaro model order based technology is the only independent industrial netlist reduction technology available, which applies on post-layout extracted netlists. Jivaro remodels and reduces the parasitic overhead in a way that the simulation tool in place is able to simulate larger netlists, or reduce the memory footprint and the execution time. Jivaro technology is used in production in more than 20 different sites worldwide and is integrated into other company's tools such as those of [Helic](#) and [Agilent](#).

### About EDXACT

Founded in 2004, EDXACT SA focuses on electronic design tools aimed at physical verification tasks. EDXACT's innovative model order reduction technology helps accelerate extensive backend verifications in complex IC design cycles. EDXACT is headquartered in Grenoble area, France with sales offices in Japan, Korea, Israel, Taiwan.

For additional information: <http://www.edxact.com>

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