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edXact, Minalogic Develop CILOE for Massively Parallel Software

Posted by EDA Geek News Staff in [EDA Tools](#) on Monday, January 19, 2009
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Backend verification specialist edXact SA announced that it is a major contributor in the joined development and innovative business model project, dubbed CILOE, led by Minalogic to help SMEs to develop massively parallel and optimized versions of their software on threaded processors and computing farms. CILOE ("Calcul Intensif pour les Logiciels de CAO Électronique et les applications embarquées"), translates to: "High Performance Computation for EDA and embedded applications") aims at developing a complete computational infrastructure, including methodologies, software, and security mechanisms in order to facilitate small and mid-sized enterprises (SME) access to this kind of technology. These tools are so compute-intensive that they require supercomputers, clusters and grids, which are out of range for SME mainly for financial reasons. Members of the cluster include two major French companies (BULL, CS-SI), three Small and Mid-Sized Enterprises (SME) (namely edXact, Infiniscale, Probayes) and several local research laboratories and universities, most importantly INRIA and CEA.

edXact will fully benefit from CILOE.

edXact's Jivaro and Comanche software tools handle large amounts of netlist data, RCLK parasitics, which need to be treated by compute intensive matrix operations. By using clustered versions of the tools on HPC infrastructure, the nightly regression tests will be done faster, thus reducing the time necessary for quality assurance before the release of a new software version.

CILOE will also help edXact to develop and deliver versions of its software tools that take advantage of parallel processing. edXact addresses this topic in partnership with BULL and several local universities and research institutions, who gained reputation for excellence in High Performance Computation .HPC versions of edXact's tools will be available at the end of the project.

"But HPC is only half of the content of this project," states Mathias Silvant, President and CEO of edXact. "The other half of it is to partner with a team that is capable of setting up a secured environment for an independent computing farm, which can be used in order to offer Software as a Service (SaaS), especially for small and mid-sized companies." This approach will open the way to more flexible licensing mechanisms than the existing ones.

The prototypes of the tools of the CILOE partners will run on the Hewlett Packard cluster for Minalogic.

The project started in September 2008 with duration of three years. The consortium is investing more than 6 Million Euros into CILOE, partially financed by European funds (FEDER). EDXACT is one of the most contributing partners accounting for about 30% of the investment.

About EDXACT

Founded in 2004, edXact SA focuses on electronic design tools aimed at physical verification tasks. edXact's innovative model order reduction technology helps accelerate extensive backend verifications in complex IC design cycles. edXact is headquartered in Grenoble area, France with sales offices in Japan, Korea, Israel and USA.

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- [edXact Appoints EDA Sales, WIN Technology as Distributors](#)
- [e2v Selects EdXact's Jivaro Netlist Reduction Tool](#)
- [EDXACT Rolls Out JIVARO HD 4.0, COMANCHE 1.1](#)
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Embedded Star

[Seven Ways to Become a Highly Effective Developer](#)
 The Seven Habits of Highly Effective People, written by Stephen R. Covey in 1989, has helped millions of people establish great habits for achieving true interdependent effectiveness in their life and their jobs. This article, Seven Ways to Become a Highly Effective Developer, will discuss the seven habits and frame them for highly effective developers.

EDA Blog

[Electronic Design Automation Industry - Q3 2008](#)

The Q3 2008 electronic design automation (EDA) industry revenue declined by 10.9% to \$1,258.6 million compared to \$1,412.1 million in Q3 2007. The four-quarter moving average declined 2.8%. Companies that were tracked employed 28,176 professionals in Q3 2008, up 3.4 percent from the 27,254 employed in Q3 2007, and 6% over the 28,004 employed in Q2, 2008.

FPGA Blog

[Xylon logiTAP Platform](#)

Xilinx announced the Xylon logiTAP platform for implementing high quality, low-cost graphical user interfaces (GUI) in embedded systems using low-cost Xilinx Spartan-3E field programmable gate arrays (FPGAs). The logiTAP platform is a full-featured system on programmable chip (SoPC) solution with a touch screen display that enables rapid, cost-effective development, prototyping, and deployment of graphical human-machine interfaces (HMIs) targeting high-volume electronics applications.

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