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TRADE NEWS: Agilent Technologies' Parasitic Reduction Tool Enhances RFIC Simulation Speed, Capacity While Preserving Accuracy

SANTA CLARA, Calif., Nov. 4, 2008 -- Agilent Technologies Inc. (NYSE: A) today announced [Jivaro-for-GoldenGate](#), a parasitic model order-reduction tool designed for use with the company's GoldenGate RF simulator software. Jivaro-for-GoldenGate is expected to enhance RFIC simulation speed and capacity with negligible loss in accuracy.

“Efficiently handling circuit simulation with an increasingly large number of extracted parasitics has pushed the envelope of simulator technology,” said Paul Colestock, RFIC marketing manager with Agilent's EEs of EDA division. “Unlike their digital counterparts, RFIC designers need to simulate circuits with self and mutual inductance in addition to parasitic resistance and capacitance. This creates a simulation-capacity requirement that is either unsupported with current hardware or requires unreasonable simulation times. Combining GoldenGate with Jivaro-for-GoldenGate solves this problem.”

Jivaro-for-GoldenGate is designed in cooperation with edXact, a company that provides high-precision, high-performance technology for

backend physical verification. edXact's Jivaro-A netlist-reduction engine is the core of this new RFIC design software.

Tailored to the challenging demands of large, complex RFIC designs, Agilent's [GoldenGate](#) is the leading RFIC simulation and analysis solution featuring unique harmonic-balance capabilities. Combining Agilent's GoldenGate simulator with Jivaro-for-GoldenGate increases simulation capacity, reduces memory requirements and boosts simulation speed when simulating large parasitic-dominated post-layout netlists.

“Jivaro-for-GoldenGate fills the gap between circuit extraction and circuit simulation created by an increased number of parasitic components (resistance, capacitance, self and mutual inductance) required to model the physical reality of circuit interconnects, substrates or packages,” said Mathias Silvant, edXact president. “Jivaro-for-GoldenGate delivers simulation speed and capacity with negligible loss of accuracy by optimizing the parasitic models against the need to keep the accuracy of the netlist predictable at a very high level.”

For more information about Jivaro-for-GoldenGate visit www.agilent.com/find/eesof-goldengate-jivaro.

To request a demo of Jivaro-for-GoldenGate, visit www.agilent.com/find/eesof-goldengate-demo-pr.

A high-resolution image of Agilent's Jivaro-for-GoldenGate software is available at www.agilent.com/find/eesof-jivaro-for-goldengate_images.

U.S. Pricing and Availability

Agilent's Jivaro-for-GoldenGate is available now, with prices starting at approximately \$30,000.

About edXact

Founded in 2004, edXact SA focuses on electronic design tools aimed at physical verification tasks. edXact's innovative model order reduction technology helps accelerate extensive backend verifications in complex IC design cycles. edXact is headquartered in Grenoble, France, with sales offices in Japan, Korea, Israel and the U.S. For more information about edXact, visit www.edxact.com.

About Agilent EEsof EDA Software

Agilent EEsof EDA is an industry-leading provider of RF-mixed signal circuit and system-design software. Agilent EEsof EDA software is compatible with and is used to design the company's test and measurement equipment. Additional information is available at www.agilent.com/find/eesof.

About Agilent Technologies

Agilent Technologies Inc. (NYSE: A) is the world's premier measurement company and a technology leader in communications, electronics, life sciences and chemical analysis. The company's 20,000 employees serve customers in more than 110 countries. Agilent had net revenue of \$5.4 billion in fiscal year 2007. Information about Agilent is available on the Web at www.agilent.com.

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