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POLL

Edxact joins Synopsys in-Sync Interoperability program

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07/21/2008 10:37 AM



PARIS — Edxact SA, French EDA startup specializing in parametric extraction and physical verification, announced it has joined Synopsys' in-Sync Interoperability program.

Synopsys noted that the in-Sync program aims to establish relationships with EDA vendors to identify, facilitate and help develop optimal joint flows and solutions that result in improved tool interoperability and maximized productivity for their mutual customers. Among other European members to the program are Esterel Technologies S.A., ProDesign GmbH, Target Compiler Technologies NV, Temento Systems, Ltd.

By joining the in-Sync Interoperability program, Edxact (Voiron, France) said it gains access to Synopsys' timing analysis and fastspice tools, as well as its parasitic extraction toolset so as to create and test a smooth interoperable design flow between Edxact's Jivaro netlist reduction tool and Synopsys' products.

Commenting on the announcement, Mathias Silvant, Edxact's president, said the company's membership to the in-Sync program "means that a number of customers who are demanding or using the proposed flows will benefit from our collaboration."



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