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e2v selects French startup's netlist reduction tool

[Anne-Francoise Pele](#)

[EE Times Europe](#)

11/06/2007 10:22 AM

PARIS – Sensors, semiconductors and electronic tubes specialist e2v (Chelmsford, England) has selected the netlist reduction tool from Edxact SA, French EDA startup specializing in parametric extraction and physical verification, to verify its next generation high speed A/D converters.

Based on advanced SiGe BiCMOS 0.18-micron process from RF and analog foundry Jazz Semiconductor, e2v's new converter family is said to include quad-8 bits devices capable of delivering conversion rates from 1.25 Gbps to 5 Gbps from a single chip at 1W per channel.

e2v indicated that it has chosen EdXact's Jivaro netlist reduction tool to help accelerate post-layout verifications on the A/D converters design. EdXact (Voiron, France) claimed that, on that A/D mixed signal design, Jivaro-enabled simulations proved to execute 3 to 10 times faster, with the same level of accuracy, thus allowing more verifications to be executed and larger blocks to be verified, while still meeting tape-out delivery schedules.

"Thanks to Jivaro, we were able to meet our just in time constraints, and to increase our verification targets as well," confirmed Daniel Boisgontier, manager of e2v's Broadband Data Converter development team. "We've chosen EdXact's tools after thorough evaluation and found their

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Here are a selection of news stories published in the latest print issue of EE Times Europe.



[NXP shifting chip design to India](#)

Development of several product lines at NXP Semiconductors' home electronics division unit are being moved out of Europe, mostly to Asian countries, according to Christos

Lagomichos, executive vice president and general manager of the division.



[Could Europe's new 'Blue Card' cause global tech talent to shun U.S.?](#)

The European Union hopes that its proposed blue-card program will provide a more attractive alternative to the U.S. green-card program, which critics say is plagued by backlogs, cumbersome processes, and insufficient quotas.

POLL

For those of you involved in embedded systems development; which of the following types of operating system are you planning to use in your next project:

- In-house developed OS
- Commercial proprietary OS
- Linux
- My project doesn't need an OS

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technology achieves amazing speed improvements, and maintains a very high accuracy.”



[U.K. kids get RFID chips in school uniforms](#)

Trevor Darnborough, whose company, Darnbro, filed for a patent on securing RFID tags to clothing, hopes other schools will be interested.



[Medea out, Catrene in](#)

Catrene - the Cluster for Application and Technology Research in Europe on NanoElectronics - is to be the \$8.5 billion European collaborative research program to take electronics into the nanoscale era.



[Skype-based mobile phone set to debut](#)

The eBay subsidiary will work with U.K.-based 3 Mobile to release the "white phone" later this month in four countries.



[Shakeup hits Q3 chip vendor ranking](#)

Intel and Samsung remained the world's first and second largest IC makers in Q3 but Toshiba moved past Texas Instruments and STMicroelectronics to take third spot, according to



[Silicon 60 version 6.1](#)

The EE Times 60 Emerging Startups list, first published in April 2004, has been updated to version 6.1 to reflect the latest corporate, commercial, technology and market conditions. Readers are encouraged to nominate startups for the next iteration of the list.

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