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Edxact demo's enhanced netlist reduction tools at DAC

[Anne-Francoise Pele](#)

[EE Times Europe](#)

05/29/2007 4:46 PM

PARIS – Edxact SA, a French EDA startup company specializing in parametric extraction and physical verification, announced it would demonstrate new versions of its Jivaro netlist reduction technology and Comanche interconnection analysis tool at the Design Automation Conference early June in San Diego, California.

Edxact (Grenoble, France) said it has enhanced its proprietary technology to confer increased capacity to its Jivaro and Comanche tools. The Grenoble-based company indeed claimed version 4.0 of Jivaro HD (High Density), whose capacity now amounts to several tens of Gigabytes for nets with RLCK elements, is suitable for designs targeting 90-nm, 65-nm geometries and below.

Edxact added that Jivaro HD brings another capacity enhancement regarding the length of the nets that can be handled and the number of ports to one and the same net, as well as the number of parasitic elements accepted on the same net.

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The French startup indicated that Comanche 1.1 is now able to compute S-parameter data between ports in high frequency circuits. Outputs are in Touchstone compatible format, so that the designer can enhance its HF simulations with detailed S-parameters computed by Comanche.

By taking parasitic netlists and computing global values of interconnection resistance, impedance and delay, as well as S, Y and Z parameters for sets of ports, Edxact claimed

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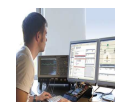
At the DATE conference in Nice, France, an interactive panel of experienced system designers and suppliers from across the automotive electronics design and supply chain, identified today's critical system design challenges facing automotive electronics and suggested approaches to cope with ever-increasing complexity of automotive electronics systems.

WaveIntegrity™ Block Diagram



[Startup readies full-chip noise analysis platform](#)

Seeking to address chip design signal integrity challenges, French electronic design automation startup Coupling Wave Solutions SA (CWS) has introduced a software platform that supports integration of sensitive functions in digital integrated circuits design by providing noise analysis across the entire system.



[Rester les maîtres du jeu de cartes à puce](#)

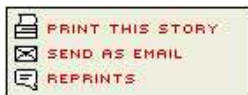
Le récent engouement pour le développement et la mise en œuvre des systèmes à base de cartes à puce à travers le monde a entraîné le développement concomitant des normes de cryptologie. La puissance de ces solutions de cryptographie, alliée à leur mise en œuvre efficace, sont deux facteurs déterminants pour gagner la confiance du public et assurer le

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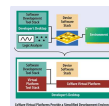
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"Comanche is a powerful analysis tool helping to rapidly pinpoint gross violations before simulation."



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succès futur des cartes à puce. Seul un travail de R&D intense et approfondi permettra d'éprouver et d'améliorer ces systèmes.



[Le développement logiciel à partir de simulations : un must pour rester compétitif](#)

Les développeurs de logiciels chargés de mettre au point des dispositifs électroniques tels que les téléphones mobiles, les dispositifs numériques grand public, automobiles ou réseaux se heurtent chaque jour au défi posé par le développement inter-plates-formes. Dans ce cadre, le logiciel est développé sur un micro-ordinateur hôte, tandis que la plate-forme d'exécution définitive est une plate-forme de composants matériels enfouis.



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il est désormais possible, où que l'on se trouve, de rester connecté et d'accéder à tout type de contenu. Les usages en matière de mobilité ont évolué ; ils dépassent désormais le champs des applications professionnelles comme la messagerie. Les progrès les plus récents concernent bien entendu les contenus vidéos sur plates-formes portables, qu'il s'agisse de contenus vidéo gratuits ou payants comme les clips musicaux, les films, ...

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