

[EE Times: Latest News](#)

European EDA vendors team on inductance

[Peter Clarke](#)

(11/16/2005 8:25 AM EST)

LONDON — Greek startup Helic SA, which has evolved into an EDA company, has teamed up with another European EDA startup EdXact SA to create an extension to Helic's VeloceRF inductance modeling, verification and synthesis tool.

Helic (Athens, Greece) launched VeloceRF in [December 2003](#). The extension to the product, called VRFJ, is based on the Jivaro netlist reduction technology from EdXact (Grenoble, France), and is due to become available in December 2005.

RFIC designers are expected to use VRFJ to reduce the element count of inductor-heavy model netlists by up to 80 percent, the two companies said in a joint statement.

Simulation results of "VRFJ-compressed" model netlists that are extracted at the post-layout phase of the design cycle would exhibit negligible discrepancy compared to the original "uncompressed" ones even for the most demanding non-linear analyses, the companies said.

"VeloceRF has been renowned for the extraction speed and accuracy of its models. Combined with VRFJ, simulation time is slashed by more than 90 percent even in the most demanding analyses," said Yorgos Koutsoyannopoulos, Helic's chief executive officer, in the same statement.

All material on this site [Copyright © 2005 CMP Media LLC](#). All rights reserved.

[Privacy Statement](#) | [Your California Privacy Rights](#) | [Terms of Service](#)